

**ANNOTATED VERSION OF MODIFIED CLAIMS TO SHOW CHANGES MADE**

1. (Once Amended) A wireless communication device, comprising:  
a serial interface[, the serial interface] configured to accept input data at a first voltage, the input data including a control signal for an integrated circuit component;  
a local level shifter[, the local level shifter] configured to accept a portion of the control signal at the first voltage, the local level shifter configured to maintain a shifted control signal, where the shifted control signal is at the operating voltage of the integrated circuit component, and where the shifted control signal controls the operation of the integrated circuit component; and  
a data latch[, the data latch] configured to accept the portion of the control signal at the first voltage level from the serial interface, the data latch configured to output the portion of the control signal at the first voltage to at least the local level shifter, where the local level [shifters] shifter is configured to maintain the shifted control signal while [the integrated circuit] at least a portion of the wireless communication device is operating in a standby mode.

2. (Once Amended) The wireless communication device of claim 1, further comprising:

a second local level shifter, the second local level shifter configured to accept a second portion of the control signal at the first voltage, the second local level shifter being configured to maintain a second shifted control signal, where the second shifted control signal is at the operating voltage of a second integrated circuit component, where the second shifted control signal controls the operation of the second integrated circuit component;

a second data latch, the second data latch configured to accept the second portion of the control signal at the first voltage level from the serial interface, the second data latch configured to output the second portion of the control signal at the first voltage level to the second local level shifter, where the second local level [shifters] shifter is configured to maintain the second shifted control signal while the [integrated circuit] at least a portion of the wireless communication device is operating in the standby mode.

7. (Once Amended) The wireless communication device of claim 1, where the [integrated circuit] at least a portion of the wireless communication device is a radio frequency integrated circuit.

10. (Once Amended) A system for maintaining [radio frequency] programming information in an integrated circuit [programming] during a standby mode, comprising:

means for accepting input data at a first voltage, the input data including control signals for a plurality of integrated circuit components;

means for distributing the control signals to the plurality of integrated circuit components;

means for converting the control signals at the first voltage to shifted control signals at the operating voltage of the integrated circuit components; and

means for maintaining the shifted control signals at the integrated circuit components during the standby mode.

18. (Once Amended) A method for maintaining [radio frequency] programming information in an integrated circuit [programming] during a standby mode, comprising the steps of:

accepting integrated circuit input data at a first voltage, the input data including control signals for a plurality of integrated circuit components;

distributing the control signals to the plurality of integrated circuit components;

converting the control signals at the first voltage to shifted control signals at the operating voltage of the integrated circuit components; and

maintaining the shifted control signals at the integrated circuit [component] components during the standby mode.

26. (Once Amended) A computer readable medium having a program for maintaining [radio frequency] programming information in an integrated circuit [programming] during a standby mode, comprising:

logic for accepting input data at a first voltage, the input data including control signals for a plurality of integrated circuit components;

logic for distributing the control signals to the plurality of integrated circuit components;

logic for converting the control signals at the first voltage to shifted control signals at the operating voltage of the integrated circuit components; and

logic for maintaining the shifted control signals at the integrated circuit components during the standby mode.